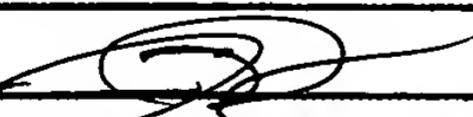


<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				<i>Complete if Known</i>	
				Application Number	Based on 09/714,891
				Filing Date	November 17, 2000
				First Named Inventor	Koji ONO et al.
				Group Art Unit	2815
				Examiner Name	Jerome Jackson Jr.
Sheet	1	of	3	Attorney Docket Number	0756-7186

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No. ¹	U.S. Patent Documents		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code ² (if known)		
P		5,643,826		Ohtani et al.	07/01/97
P		5,923,962		Ohtani et al.	07/13/99
P		5,247,190		Friend et al.	09/21/93
P		5,399,502		Friend et al.	03/21/95

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Examiner Initials	Cite No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Office ³	Number ⁴	Kind Code ⁵ (if known)	
P		JP	07-130652		05/19/95
P		JP	08-078329		03/22/96
P		JP	10-135468		05/22/98
P		JP	10-135469		05/22/98
P		JP	10-294280		11/04/98
P		JP	11-191628		07/13/99
P		JP	10-092576		04/10/98
P		JP	11-345767		12/14/99
P		JP	11-354442		12/24/99
		pct	WO90/13148		11/01/90

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P		Shimokawa et al., "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, Vol. 27, No. 5, May, pp. 751-758, 1988.			
P		Hatano et al., "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT With High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.			
P		Hermann Schenk et al., "Polymers for Light Emitting Diodes," EuroDisplay '99 Proceedings, pp. 33-37.			

Examiner Signature		Date Considered	2/19/05
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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		Number	Kind Code ² (if known)		
2		4,394,182		Maddox III	07/19/83
2		5,528,397		Zavracky et al.	06/18/96
2		5,583,369		Yamazaki et al.	12/10/96
2		5,686,328		Zhang et al.	11/11/97
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2		5,736,750		Yamazaki et al.	04/07/98
2		5,830,787		Kim	11/03/98
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2		6,001,714		Nakajima et al.	12/14/99
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		Office ³	Number ⁴		
2		JP	09-191111		07/22/97
2		JP	09-293600		11/11/97
2		JP	10-104659		04/24/98
2		JP	10-233511		09/02/98
2		JP	04-369271		12/22/92

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2		Specifications and Drawings for Application Serial No. 09/619,732, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 19, 2000.			
2		Specifications and Drawings for Application Serial No. 09/618,930, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 18, 2000.			
2		Specifications and Drawings for Application Serial No. 09/433,705, "Method of Fabricating a Semiconductor Device," Filed on November 4, 1999.			

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STATEMENT BY APPLICANT

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Sheet

3

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7		5,323,042		Matsumoto	06/21/94	
7		6,198,133	B1	Yamazaki et al.	03/06/01	
7		6,281,552	B1	Kawasaki et al.	08/28/01	
7		6,166,414		Miyazaki et al.	12/26/00	
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7		JP	05-102483			04/23/93		Full
7		JP	8-274336			10/18/96		Full
7		JP	7-235680			09/05/95		Full
7		JP	6-148685			05/27/94		Full
7		EP	1 003 223		Yamazaki	05/24/00		Eng.
7		EP	1 049 167		Yamazaki	11/02/00		Eng.

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